JASON PIEN

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Education

- **Brown University** — Providence, RI
  Bachelor of Science in Computer Engineering; GPA: 3.97 — *Expected May 2025*
  - **Relevant Coursework**: Design of Computing Systems, Digital Electronics Syst Design, Deep Learning, Data Structure & Alg, Computer Vision — To be taken fall 2024: Embedded Microprocessor Design, Multiprocessor Synchronization
  - **Tau Beta Pi (President)**: Engineering Honors Society, initiated in 2023 as top 1/8th of the junior class for academic excellence in engineering
  - **FinTech@Brown Club (Head of Technology)**: Incoming E-board member into fall 2024

Skills / Qualifications

- Proficient in digital Verilog design, optimizing **RTL design** (both area and speed)
- Proficient in **Python** and **Verilog HDL**. Able to use Java, C, RISC-V Assembly, JavaScript, HTML5/CSS, Matlab, and any other programming languages with reference to documentation
- Familiar with **Intel Quartus Prime 20.1**, **ModelSim**, TensorFlow 2.15.0, Git, Google CoLab, LTSpice, **Altera Cyclone V FPGA**, ESP32, ARM Cortex-M boards

Relevant Experience

- **Incoming Software Engineering Intern** — New York City, NY
  Ernst & Young — *Summer 2024*
  - Learning and implementing next-generation technologies within leading investment banks, insurers, and investment managers
  - Implementing applications within decentralized finance and edge computing
- **FPGA RISC-V Pipelined Processor Design (Student and TA)** — Providence, RI
  Incoming Teaching Assistant — *Jan 2023 - Present*
  - Attained a 1.26x speed-up over a single-cycle design
  - Developed a 2-stage pipeline design in Verilog HDL from scratch, validated in ModelSim, and deployed on an Altera Cyclone V DE0-CV FPGA
  - Reached a 2.52x speed-up compared to the target performance
  - Placed 2nd for runtime, (7% slower than the 1st place)
  - Incoming TA in the second year, preparing baseline so that students can implement more types of optimizations
- **Freedays Venture** — Providence, RI
  Co-founder — *Jun 2023 - Present*
  - Developed an initial proof of concept breadboard prototype using C and ESP32, before transitioning to software
  - Implemented OpenAI API to gpt-3.5-turbo, developed scripts for data cleansing and fine-tuned the application model
  - Click here to watch me give the pitch in front of whole school

Projects

- **FPGA Stopwatch**: Altera Cyclone V DE0-CV FPGA w/ Verilog HDL within Intel Quartus Prime 20.1, I developed a FSM hardware stopwatch with logic support to a full binary to BCD converter outputting to 6 7-segment HEX displays.
- **AI Daily Tech News Compiler Bot (T.I.D.A.L**: Developed an AI-powered newsletter summarization bot, "TIDAL", to automate daily consumption and gathering of tech industry news. Leveraged web scraping and natural language processing to extract key details and generate concise article summaries, enabling efficient information digestion. Used Telegram API to send summaries to my phone.
- **jasonpien.com Website**: Implemented & deployed personal website in 3 weeks. Self-taught HTML5/CSS and JavaScript.

Awards

- **Brown Entrepreneurship Scholarly Award**: Awarded $4000 as a recognition and grant for being accepted into Brown's elite startup accelerator program
- **Explore Grant**: Awarded $250 to fund a team's early discovery work
- **National Collegiate Taekwondo Bronze Medalist**: National Collegiate Taekwondo Championship, Bantam Weight black belt sparring